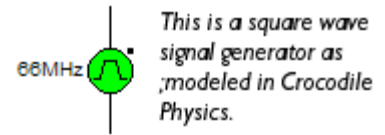


# Flip-Flop Logic Problems

Develop solutions to two of the following problems using sequential logic (flip-flops).

- The security company responsible for security at the Sky Dome has decided to give extra scrutiny to every 7<sup>th</sup> person to come through each turnstile. They have commissioned you to design a circuit which will turn on an indicator light when a switch on the turnstile is activated seven times, and then reset to 0. (Use only Flip-flop circuits, not counters). How would you modify your circuit if the company wished to identify every 5<sup>th</sup> visitor.
- In a computer system, the system clock has a frequency of 66.6 MHz. The PCI bus operates at 33.3 MHz and the ISA bus operates at 8.33 MHz. Design a circuit which will have the 66.6 MHz signal as the input and outputs of 33.3 MHz and 8.33 MHz.
- Competitors in a race must carry a small electronic device which has four key switches. A judge at each checkpoint will insert a key in a different switch and then remove it. Create a circuit which will turn on a light when all four keys have been inserted.
- A security system has four input buttons - A,B,C,D – and a reset button. Design a circuit which will turn on an output to unlock the door only if the buttons are pressed in the sequence CBDA. Bonus marks: disable the circuit after three incorrect tries.



You may test your circuit using Crocodile Clips, but your solutions are to be presented as logic drawings on paper. All components, inputs and outputs on the drawing should be labeled, and an explanation of how the circuit works should accompany each circuit. Marks will be awarded for originality and circuit enhancements, so be careful what you share with your classmates.

## Evaluation Rubric

|   | Level 1 (50-59%)  | Level 2 (60-69%)                                       | Level 3 (70-79%)                                     | Level 4 (80-100%)   | Value |
|---|---|--|--|---|-------|
| <b>Problem Solving (Thinking &amp; Inquiry)</b> | Attempted solutions to two problems with limited success. | Workable solutions, but partial or needlessly complex. | Correct, workable, simple solutions to two problems. | Correct, original, elegant solutions with extra features. | 10    |
| <b>Circuit Design (Application)</b>             | Circuits incorrect or impossible to understand.           | Circuits correct but too complex or hard to follow.    | Economical, simple, understandable circuit layout.   | Exceptionally well-laid out circuits.                     | 5     |
| <b>Explanation (Knowledge)</b>                  | Incomplete and inaccurate explanations.                   | Incomplete or inaccurate explanations.                 | Accurate, complete explanation of circuit operation. | Explanation shows insight and analytic skills.            | 5     |
| <b>Drawings (Communication)</b>                 | Drawings are sloppy and unlabelled.                       | Drawings are sloppy or unlabelled.                     | Neat, clean drawings with all parts labeled.         | Exceptionally well-organized and labeled drawings         | 5     |
| <b>Total</b>                                    |   |  |  |   | 25    |